

F2F Decoder IC

Triple track, BS300HP

Features

- Noise Filter technology
- Auto-gain control technology
- Operational power DC 2.7V ~ 5.5V
- Power down mode for stand-by control
- Built-in operational amplification circuitry.
- Old/Bad issued card reading improvement
- Support for 75/210 BPI recording density.
- Speed range: 300 to more than 12600 BIT/sec.
- Small outline and surface mount design. LQFP48.
- 0.6um CMOS ASIC IC for F2F magnetic stripe card reader.
- Two modes for RCP width. One is 14 ~ 60us. The other one is 1/4 RDP width.
- Low power: Current Max. 3.3mA, best for portable, hand-held devices.
- Built-in Boarding Pass Ticket reading solution---recovery for data loss.



LQFP48 7x7mm

Application

- Magnetic stripe card reader.
- POS Keyboard
- Security Control.
- CAT (Card Authorization Terminal)

Specification

- Power Voltage: DC2.7V ~ 5.5V
- Consumption: 2.5mA /DC 3.3V; 3.3mA /DC 5.0V
- Reading Speed Range /second: 10cm/sec ~ 150cm/sec
- MTBF: 100,000 hrs
- Environment : Temperature: Operating: -10°C ~ 55°C; Storage: -30°C ~ 70°C
Humidity: Operating: 10% ~ 90%; Storage: up to 100%

Pin Assignment

No	Signal	No.	Signal	No.	Signal	No.	Signal	No	Signal	No	Signal
1	NC	9	OPB1OUT	17	MSIC+	25	NC	33	NC	41	OSC
2	OPA2OUT	10	OPB2IN	18	MSIC-	26	RDPB	34	CLS	42	RESEL
3	OPA2IN	11	OPB2OUT	19	OPC1OUT	27	RCPB	35	NC	43	IBS
4	OPA1OUT	12	NC	20	OPC2IN	28	RDPC	36	NC	44	NC
5	MSIA-	13	NC	21	OPC2OUT	29	RCPC	37	NC	45	VDD
6	MSIA+	14	NC	22	NPWDN	30	RDPA	38	ADJ	46	VCC
7	MSIB+	15	GND	23	NC	31	RCPA	39	NRCPM	47	Test
8	MSIB-	16	VREFIN	24	NC	32	VSS	40	NERC	48	NC